

IN THE UNITED STATES PATENT OFFICE

Applicants: Michael J. Hart et al.
Assignee: Xilinx, Inc.
Title: "Structures and Methods for Selectively Applying a Well Bias to Portions of a Programmable Device"
Serial No.: Not Yet Known File Date: 07-21-03
Examiner: Not Yet Known Art Unit: Not Yet Known
Div. of
Serial No.: 09/956,203 File Date: 09-18-01
Docket No.: X-865-1D US

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INFORMATION DISCLOSURE STATEMENT

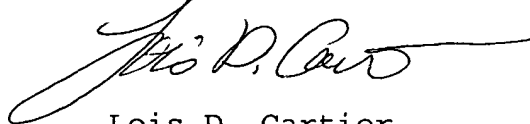
Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant(s) bring(s) to the attention of the Examiner the six (6) references listed in the attached Substitute for Form PTO-1449 (Information Disclosure Statement by Applicant).

All of these references were cited in prior related U.S. patent application Serial Number 09/956,203 filed September 18, 2001 to which this application claims priority. Copies of these references have not been supplied herein since they were previously submitted in the parent case.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully Submitted,



Lois D. Cartier
Agent for Applicants
Reg. No. 40,941

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Examiner Signature		Date Considered	
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